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INDICATION FORM

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NEY DRESS M	Application Number	10/822,544
	Filing Date	April 12, 2004
	First Named Inventor	Peter Pfann et al.
	Title	INTEGRATED TRANSCEIVER CIRCUIT
	Art Unit	2681
	Examiner Name	
	Attorney Docket Number	EHFP139US

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Applicant/Inventor.						
Assignee of record of the entire interest. See 37 CFR 3.71. Statement under 37 CFR 3.73(b) is enclosed. (Form PTO/SB/96)						
SIGNATURE of Applicant or Assignee of Record (if assignee, put name, title and company name in the "Name" space below)						
Name Infineon Technologies AG						
Signature S. Girstenbreu i. V. Chioliphy H. Post i. V. Post			1.U. PO60			
Date Munich, November 22,200	4	Telephone				
NOTE: Signatures of all the inventors or assignees of record of the entire interest or their representative(s) are required. Submit multiple forms if more than one signature is required, see below.						
*Total of forms are submitted.						

This collection of information is required by 37 CFR 1.31 and 1.33. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 3 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

PTO/SB/96 (06-04)

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THE THE PARTY OF T	STATEMENT	UNDER 37 CFR 3.73(b)
Applicant/Patent Owner:	Peter Pfann et al.	
Application No./Patent No.:	10/822,544 File	ed/Issue Date: April 12, 2004
Entitled: INTRGRATED TRAN	ISCEIVER CIRCUIT WITH LOW	INTERFERENCE PRODUCTION AND SENSITIVITY
Infineon Technologies (Name of Assignee)	AG, a	Corporation (Type of Assignee, e.g., corporation, partnership, university, government agency, etc.)
states that it is: 1. In the assignee of the er	ntire right, title, and interest; o	r
The extent (by percen	an the entire right, title and in stage) of its_ownership interes ent identified above by virtue	t is ———— %
A. [] An assignment from t in the United States Paattached.	he inventor(s) of the patent ap atent and Trademark Office at	pplication/patent identified above. The assignment was recorded t Reel, Frame, or for which a copy thereof is
OR		
B. [] A chain of title from the below:	e inventor(s), of the patent ap	plication/patent identified above, to the current assignee as shown
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[NOTE: A separate copy submitted to Assignment	or other documents in the chaing (i.e., a true copy of the origing the original of the USPTO. See MPEP 30	nal assignment document(s)) must be 37 CFR Part 3, if the assignment is to be
The undersigned (whose title	e is supplied below) is authori	ized to act on behalf of the assignee.
<u>Munich, Novemb</u>	er 22,2004	S.Girstenbreu H.Post
Date		Typed or printed name
Telephone num	nber	V Signature
		(both authorized patent adminis)
		Title

This collection of information is required by 37 CFR 3.73(b). The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

ASSIGNMENT

For good and valuable consideration, I, Peter Pfann, a citizen of Germany, residing at Konradstr. 16, 80801 München, Germany, and Zdravko Boos, a citizen of Germany, residing at Scheibmeirstr. 16. 81827 München, Germany, hereinafter individually or collectively referred to as "Assignor";

Hereby sell, assign and transfer to Infineon Technologies AG, a corporation organized and existing under the laws of Germany, having its principal place of business at St.-Martin-Str. 53, 81669 Munich, Germany, hereinafter "Assignee", its successors, assigns and legal representatives, the entire right, title and interest in and for the United States, in and to any and all improvements which are disclosed in the application for United States Letters Patent, which has been executed by the undersigned and

entitled: INTEGRATED TRANSCEIVER CIRCUIT WITH LOW INTERFERENCE PRODUCTION AND SENSITIVITY

and in and to said application and all divisional, continuing, substitute, renewal, reissue, and all other applications for Letters Patent which have been or shall be filed in the United States on any of said improvements; and in and to all original and reissued patents which have been or shall be filed in the United States on said improvements;

Agree that said Assignee may apply for and receive Letters Patent for said improvements in its own name; and that, when requested, without charge to but at the expense of said Assignee, its successors, assigns and legal representatives, to carry out in good faith the intent and purpose of this assignment, the undersigned will execute all divisional, continuing, substitute, renewal, reissue, and all other patent applications on any and all said improvements; execute all rightful oaths, assignments, powers of attorney and other papers; communicate to said Assignee, its successors, assigns, and legal representatives, all facts known to the undersigned relating to said improvements an the history thereof; and generally do everything possible which said Assignee, its successors, assigns or legal representatives shall consider desirable for aiding in securing and maintaining proper patent protection for said improvements and for vesting title to said improvements and all applications for patents and all patents on said improvements, in said Assignee, its successors, assigns and legal representatives;

Covenant with said Assignee, it successors, assigns and legal representatives that no assignment, grant, mortgage, license or other agreement affecting the rights and property herein conveyed has been made to others by the undersigned, and that full right to convey the same as herein expressed is possessed by the undersigned; and

Authorize and direct my/our attorneys to insert below* the application number and filing date of said application now identified as Attorney Docket No. P2003,0235, as soon as the same shall have been made known to them by the United States Patent and Trademark Office.

*U.S. Application No.: 10/822,544

*Filing Date:

April 12, 2004

DATE: x 7. May 2004

Infineon Ref. No. P2003,0235 O.C. Ref. No. 109612.00088

SIGNATURE: X Zdravko Boos

Havlo Ros

DATE: 7 07 May 2004

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